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GP2122

PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL  
FORM

(to be used for all correspondence after initial filing)

Application Number	09/747,824
Filing Date	December 22, 2000
First Named Inventor	Simons, John
Group Art Unit	2122
Examiner Name	Unassigned
Attorney Docket Number	16869C-017000US

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MAR 20 2002

Technology Center 2100

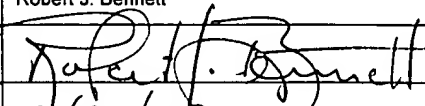
Total Number of Pages in This Submission

6

ENCLOSURES (check all that apply)

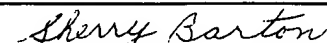
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): TRANSMITTAL FORM, Return Postcard
Remarks		The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm and Individual name	Townsend and Townsend and Crew LLP Robert J. Bennett	Reg. No. 27,533
Signature		
Date	2/26/02	

CERTIFICATE OF MAILING

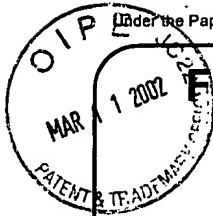
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: February 26, 2002

Typed or printed name	Sherry Barton		
Signature		Date	February 26, 2002

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# **FEE TRANSMITTAL for FY 2001**

Patent fees are subject to annual revision.

**TOTAL AMOUNT OF PAYMENT** (\$) 180

**Complete if Known**

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METHOD OF PAYMENT					FEE CALCULATION (continued)																																																																																																																																																																																		
<p>1. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge indicated fees and credit any over payments to:</p> <p>Deposit Account Number: <b>20-1430</b></p> <p>Deposit Account Name: <b>Townsend and Townsend and Crew LLP</b></p> <p><input checked="" type="checkbox"/> Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17  <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27</p>					<p>3. ADDITIONAL FEES</p> <table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th>Large Fee Code</th> <th>Entity Fee (\$)</th> <th>Small Fee Code</th> <th>Entity Fee (\$)</th> <th>Fee Description</th> <th>Fee Paid</th> </tr> </thead> <tbody> <tr><td>105</td><td>130</td><td>205</td><td>65</td><td>Surcharge - late filing fee or oath</td><td></td></tr> <tr><td>127</td><td>50</td><td>227</td><td>25</td><td>Surcharge - late provisional filing fee or cover sheet.</td><td></td></tr> <tr><td>139</td><td>130</td><td>139</td><td>130</td><td>Non-English specification</td><td></td></tr> <tr><td>147</td><td>2,520</td><td>147</td><td>2,520</td><td>For filing a request for reexamination</td><td></td></tr> <tr><td>112</td><td>920*</td><td>112</td><td>920*</td><td>Requesting publication of SIR prior to Examiner action</td><td></td></tr> <tr><td>113</td><td>1,840*</td><td>113</td><td>1,840*</td><td>Requesting publication of SIR after Examiner action</td><td></td></tr> <tr><td>115</td><td>110</td><td>215</td><td>55</td><td>Extension for reply within first month</td><td></td></tr> <tr><td>116</td><td>400</td><td>216</td><td>200</td><td>Extension for reply within second month</td><td></td></tr> <tr><td>117</td><td>920</td><td>217</td><td>460</td><td>Extension for reply within third month</td><td></td></tr> <tr><td>118</td><td>1,440</td><td>218</td><td>720</td><td>Extension for reply within fourth month</td><td></td></tr> <tr><td>128</td><td>1,960</td><td>228</td><td>980</td><td>Extension for reply within fifth month</td><td></td></tr> <tr><td>119</td><td>320</td><td>219</td><td>160</td><td>Notice of Appeal</td><td></td></tr> <tr><td>120</td><td>320</td><td>220</td><td>160</td><td>Filing a brief in support of an appeal</td><td></td></tr> <tr><td>121</td><td>280</td><td>221</td><td>140</td><td>Request for oral hearing</td><td></td></tr> <tr><td>138</td><td>1,510</td><td>138</td><td>1,510</td><td>Petition to institute a public use proceeding</td><td></td></tr> <tr><td>140</td><td>110</td><td>240</td><td>55</td><td>Petition to revive - unavoidable</td><td></td></tr> <tr><td>141</td><td>1,280</td><td>241</td><td>640</td><td>Petition to revive - unintentional</td><td></td></tr> <tr><td>142</td><td>1,280</td><td>242</td><td>640</td><td>Utility issue fee (or reissue)</td><td></td></tr> <tr><td>143</td><td>460</td><td>243</td><td>230</td><td>Design issue fee</td><td></td></tr> <tr><td>144</td><td>620</td><td>244</td><td>310</td><td>Plant issue fee</td><td></td></tr> <tr><td>122</td><td>130</td><td>122</td><td>130</td><td>Petitions to the Commissioner</td><td></td></tr> <tr><td>123</td><td>50</td><td>123</td><td>50</td><td>Petitions related to provisional applications</td><td></td></tr> <tr><td>126</td><td>180</td><td>126</td><td>180</td><td>Submission of Information Disclosure Stmt</td><td>180</td></tr> <tr><td>581</td><td>40</td><td>581</td><td>40</td><td>Recording each patent assignment per property (times number of properties)</td><td></td></tr> <tr><td>146</td><td>740</td><td>246</td><td>370</td><td>Filing a submission after final rejection (37 CFR § 1.129(a))</td><td></td></tr> <tr><td>149</td><td>740</td><td>249</td><td>370</td><td>For each additional invention to be examined (37 CFR § 1.129(b))</td><td></td></tr> <tr><td>179</td><td>740</td><td>279</td><td>370</td><td>Request for Continued Examination (RCE)</td><td></td></tr> <tr><td>169</td><td>900</td><td>169</td><td>900</td><td>Request for expedited examination of a design application</td><td></td></tr> </tbody> </table>					Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid	105	130	205	65	Surcharge - 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<p><b>1. BASIC FILING FEE</b></p> <table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th>Large Fee Code</th> <th>Entity Fee (\$)</th> <th>Small Fee Code</th> <th>Entity Fee (\$)</th> <th>Fee Description</th> <th>Fee Paid</th> </tr> </thead> <tbody> <tr><td>101</td><td>740</td><td>201</td><td>370</td><td>Utility filing fee</td><td></td></tr> <tr><td>106</td><td>330</td><td>206</td><td>165</td><td>Design filing fee</td><td></td></tr> <tr><td>107</td><td>510</td><td>207</td><td>255</td><td>Plant filing fee</td><td></td></tr> <tr><td>108</td><td>740</td><td>208</td><td>370</td><td>Reissue filing fee</td><td></td></tr> <tr><td>114</td><td>160</td><td>214</td><td>80</td><td>Provisional filing fee</td><td></td></tr> </tbody> </table> <p><b>SUBTOTAL (1)</b> (\$) <span style="border: 1px solid black; padding: 2px;">180</span></p>					Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid	101	740	201	370	Utility filing fee		106	330	206	165	Design filing fee		107	510	207	255	Plant filing fee		108	740	208	370	Reissue filing fee		114	160	214	80	Provisional filing fee																																																																																																																																																
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<p><b>2. EXTRA CLAIM FEES</b></p> <p>Total Claims <span style="border: 1px solid black; padding: 2px;">20**</span> = <span style="border: 1px solid black; padding: 2px;">20</span> X <span style="border: 1px solid black; padding: 2px;">9</span> = <span style="border: 1px solid black; padding: 2px;">180</span></p> <p>Independent Claims <span style="border: 1px solid black; padding: 2px;">3**</span> = <span style="border: 1px solid black; padding: 2px;">3</span> X <span style="border: 1px solid black; padding: 2px;">9</span> = <span style="border: 1px solid black; padding: 2px;">27</span></p> <p>Multiple Dependent <span style="border: 1px solid black; padding: 2px;"></span> X <span style="border: 1px solid black; padding: 2px;">9</span> = <span style="border: 1px solid black; padding: 2px;">0</span></p> <table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th>Large Fee Code</th> <th>Entity Fee (\$)</th> <th>Small Fee Code</th> <th>Entity Fee (\$)</th> <th>Fee Description</th> <th>Fee Paid</th> </tr> </thead> <tbody> <tr><td>103</td><td>18</td><td>203</td><td>9</td><td>Claims in excess of 20</td><td></td></tr> <tr><td>102</td><td>84</td><td>202</td><td>42</td><td>Independent claims in excess of 3</td><td></td></tr> <tr><td>104</td><td>280</td><td>204</td><td>140</td><td>Multiple dependent claim, if not paid</td><td></td></tr> <tr><td>109</td><td>84</td><td>209</td><td>42</td><td>** Reissue independent claims over original patent</td><td></td></tr> <tr><td>110</td><td>18</td><td>210</td><td>9</td><td>** Reissue claims in excess of 20 and over original patent</td><td></td></tr> </tbody> </table> <p><b>SUBTOTAL (2)</b> (\$) <span style="border: 1px solid black; padding: 2px;">180</span></p>					Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid	103	18	203	9	Claims in excess of 20		102	84	202	42	Independent claims in excess of 3		104	280	204	140	Multiple dependent claim, if not paid		109	84	209	42	** Reissue independent claims over original patent		110	18	210	9	** Reissue claims in excess of 20 and over original patent																																																																																																																																																
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**SUBMITTED BY**

Name (Print/Type) **Robert J. Bennett**

Signature *Robert J. Bennett*

Registration No. (Attorney/Agent) **27,533**

**Complete (if applicable)**

Telephone **415-576-0200**

Date **2-26-02**

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Technology Center 210C

Examiner: Unassigned

Art Unit: 2122

**INFORMATION DISCLOSURE  
STATEMENT UNDER 37 CFR §1.97 and  
§1.98**

**For: ASSEMBLY LANGUAGE CODE  
COMPILATION FOR AN  
INSTRUCTION-SET ARCHITECTURE  
CONTAINING NEW INSTRUCTIONS  
USING THE PRIOR ASSEMBLER**

Sir:

Applicant believes that no fee is required for submission of this statement, since it is being submitted prior to the first Office Action. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

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Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 2 of 3

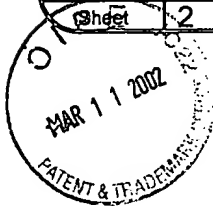
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Examiner Name	Unassigned
Attorney Docket Number	16869C-017000US

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MAR 20 2002

Technology Center 2101



**STATEMENT OF FACTS**

In or about 1994, Hitachi Microsystems Inc. (now Hitachi Semiconductor (America), Inc.), a wholly-owned subsidiary of Assignee of this application, Hitachi, Ltd., began design and development of a next generation processor that would replace a then commercially marketed processor (hereinafter, the "Earlier Processor"). It was contemplated the next generation processor would retain much of the architecture of the Earlier Processor, but would include digital signal processing (DSP) functionality. This design and development effort would also include development of a new assembler; one that would be able to not only recognize, interpret, and assemble (to machine language) assembly language instructions of the instruction set architecture used by the Earlier Processor, but also newly added instructions that would exercise the added DSP functionality of the next generation processor.

The responsibility for creating the new assembler was assigned to another wholly-owned subsidiary of Assignee, Hitachi America, Ltd. Development of the new assembler meant that it would not be available, until completed, for assembling new (DSP related) assembly language instructions for testing, debugging, and benchmarking the next generation processor design. Thus, rather than wait, the present invention was developed to incorporate the assembler for the Prior Processor into a "Spoof" assembler that was capable of handling instructions added to the new instruction set architecture to take advantage of the added DSP capability.

Examiner Signature		Date Considered	
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<sup>1</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>2</sup> Unique citation designation number. <sup>3</sup> Applicant is to place a check mark here if English language Translation is attached.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 3 of 3

### Complete if Known

Application Number	09/747,824
Filing Date	December 22, 2000
First Named Inventor	Simons, John
Group Art Unit	2122
Examiner Name	Unassigned
Attorney Docket Number	16869C-017000US

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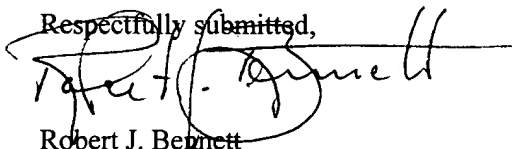
MAR 20 2002

Technology Center 2100



The Spoof assembler was never made public, never sold, and used only internally by Assignee and Assignee's wholly-owned subsidiary to test and benchmark the design of the next generation processor. In addition, speech compression programs were developed using the Spoof assembler, which were later provided to customers of the next generation processor when put on the market.

Respectfully submitted,

  
Robert J. Bennett  
Reg. No. 27,533

TOWNSEND and TOWNSEND and CREW LLP  
Two Embarcadero Center, 8<sup>th</sup> Floor  
San Francisco, California 94111-3834  
Tel: 415-576-0200  
Fax: 415-576-0300  
RJB:sb

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Examiner Signature		Date Considered	
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<sup>1</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>2</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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